

## WEST Search History

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DATE: Monday, October 04, 2010

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<i>Prior Art</i>			
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L55	L54 and (updat\$3 near5 queue\$3)	2
<input type="checkbox"/>	L54	L53 and (queue\$3 near5 descriptor\$1)	4
<input type="checkbox"/>	L53	L52 and (cache same queue\$3)	10
<input type="checkbox"/>	L52	L51 and (tail near5 pointer\$1)	45
<input type="checkbox"/>	L51	L50 and (head near5 pointer\$1)	66
<input type="checkbox"/>	L50	L37 and (count\$3 same identifier\$1)	3765
<input type="checkbox"/>	L49	L48 and (count\$3 same identifier\$1)	0
<input type="checkbox"/>	L48	L47 and (updat\$3 or modify\$3)	2
<input type="checkbox"/>	L47	L46 and head and pointer\$1	2
<input type="checkbox"/>	L46	L45 and command\$1	2
<input type="checkbox"/>	L45	L44 and cache	2
<input type="checkbox"/>	L44	L43 and fetch\$3	8
<input type="checkbox"/>	L43	L42 and de\$queue\$3	8
<input type="checkbox"/>	L42	L41 and en\$queue\$3 and @py<=2002	12
<input type="checkbox"/>	L41	L40 and (tail near5 pointer\$1)	173
<input type="checkbox"/>	L40	L39 and (queue\$1 near5 memory)	985
<input type="checkbox"/>	L39	L37 and l13	15055
<input type="checkbox"/>	L38	L37 and l26	5
<input type="checkbox"/>	L37	L36 or l35 or l34 or l33	117504
<input type="checkbox"/>	L36	711/154-156.cccls.	6307
<input type="checkbox"/>	L35	370/412.cccls.	3859
<input type="checkbox"/>	L34	710/310,54.cccls.	1140
<input type="checkbox"/>	L33	707/\$.cccls.	106782
<input type="checkbox"/>	L32	L31 and @py=2002	4
<input type="checkbox"/>	L31	L30 and L13	161
<input type="checkbox"/>	L30	L29 and (queue near5 descriptor\$1)	355
<input type="checkbox"/>	L29	(head near5 pointer) and (tail near5 pointer\$1)	2786
<input type="checkbox"/>	L28	L27 and dequeue	7
<input type="checkbox"/>	L27	L26 and enqueue	12

□	L26	(queue near5 descriptor\$1) and (head and tail) and (memory or cache) and (pointer\$1) and @py=2002	39
□	L25	(memory and head and tail).ti.	33
□	L24	L23 and (head near5 pointer\$1)	33
□	L23	L22 and (tail near5 pointer\$1)	33
□	L22	L21 and (memory same cache)	49
□	L21	L20 and (empty near5 queue)	52
□	L20	L19 and queue	62
□	L19	L18 and dequeue	62
□	L18	L17 and (head near5 tail)	62
□	L17	L16 and cache	75
□	L16	L15 and descriptor	92
□	L15	L14 and (queue same dequeue)	180
□	L14	L13 and (head near5 point\$2)	2492
□	L13	(memory near5 structure)	167094
□	L12	5671446.uref.	26
□	L11	(memory near5 head) same (queue near5 enqueue)	26
□	L10	L7 and (head near5 point\$)	11
□	L9	L7 and (dequeue\$1)	6
□	L7	(data and memory and queue\$).ti.	426
□	L6	'memory queue'.ti.	60
□	L3	(queue descriptor).ti.	24
□	L2	L1 and 711/\$.ccls.	16
□	L1	(queue descriptor).ab.	74

END OF SEARCH HISTORY